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APPLICATION No.  
09/763,204

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

APPLICANT  
Fabien Clermidy, et al.

FILING DATE  
February 20, 2001

GROUP  
2122

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
VLW	Varvarigou, T.A., et al., "New Algorithms for Reconfiguring VLSI/WSI Arrays," vol.3, no 4., 15 pages		
VLW	Kim, J.H., et al., "An On-Line Reconfiguration Algorithm of WSI Processor Arrays," vol. CONF. 25, 3 pages		
VLW	Popli, S.P., et al., "A Reconfigurable VSLI Array for Reliability and Yield Enhancement," vol. CONF. 2, 11 pages		
VLW	Clermidy, F., et al., "A New Placement Algorithm Dedicated to Parallel Computers: Bases and Application," 7 pages		
Examiner Signature	Y. Panda & L. Wilson	Date Considered	12-3-03

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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

[illegible]

**EXAMINER**

DATE CONSIDERED

(03/01)